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Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: MOU-SHIUNG LIN

For: STRAIN RELEASE CONTACT SYSTEM FOR INTEGRATED CIRCUITS

jc612 U.S. PTO
09/249252
02/12/99

Enclosed are:

- ☒ 5 sheets of drawing(s) - formal.
- ☐ An assignment of the invention to
- ☐ An associate power of attorney
- ☒ Small Entity Form

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Respectfully submitted,

STEPHEN B. ACKERMAN, REG. NO. 37,761

**STATEMENT CLAIMING SMALL ENTITY STATUS
(37 CFR 1.9(f) & 1.27(b))--INDEPENDENT INVENTOR**

Docket Number (Optional)

MSLN98-003

Applicant, Patentee, or Identifier: Mou-Shiung Lin

Application or Patent No.: _____

Filed or Issued: _____

Title: STRAIN RELEASE CONTACT SYSTEM FOR INTEGRATED CIRCUITS

As a below named inventor, I hereby state that I qualify as an independent inventor as defined in 37 CFR 1.9(c) for purposes of paying reduced fees to the Patent and Trademark Office described in:

- ☒ the specification filed herewith with title as listed above.
☐ the application identified above.
☐ the patent identified above.

I have not assigned, granted, conveyed, or licensed, and am under no obligation under contract or law to assign, grant, convey, or license, any rights in the invention to any person who would not qualify as an independent inventor under 37 CFR 1.9(c) if that person had made the invention, or to any concern which would not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e).

Each person, concern, or organization to which I have assigned, granted, conveyed, or licensed or am under an obligation under contract or law to assign, grant, convey, or license any rights in the invention is listed below:

- ☒ No such person, concern, or organization exists.
☐ Each such person, concern, or organization is listed below.

Separate statements are required from each named person, concern, or organization having rights to the invention stating their status as small entities. (37 CFR 1.27)

I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 CFR 1.28(b))

Mou-Shiung Lin
NAME OF INVENTOR

NAME OF INVENTOR

NAME OF INVENTOR

Mou-Shiung Lin
Signature of inventor

Signature of inventor

Signature of inventor

January 18, 1999
Date

Date

Date

STRAIN RELEASE CONTACT SYSTEM FOR INTEGRATED CIRCUITS

FIELD OF THE INVENTION

The invention relates to the general field of integrated circuit packaging with particular reference to the absorption of stress at the contacts.

BACKGROUND OF THE INVENTION

In the macro world (measured in fractions of an inch) spring loaded contacts are widely used because of their ability to absorb stresses associated with pressing two surfaces together. This is of particular importance if either, or both, surface is fragile and subject to damage if the contact force were to be transmitted to it. Contacting systems of this type are commonly formed by seating a ball in a cylindrical cavity, behind an opening that is slightly smaller than the diameter of the ball, and mounting a coil spring between the ball and the other end of the cylinder.

Similar stresses are associated with contacting systems that are used in the micro world (measured in microns and fractions thereof) but, to the best of our knowledge, spring loaded contacts similar to those described above have not yet been developed. A particular

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example of a micro system in which a fragile set of surfaces need to make and maintain good contact with a second set of surfaces, is a packaged integrated circuit where contact pads that are part of a chip need to make contact with solder balls located on the outside of the package. These solder balls will be used later to attach the package to the next packaging level (for example a printed circuit board) and it is important that the associated thermal and mechanical stresses not be transmitted down to the chip level.

The reason that spring loaded contacts have thus far not been used in the micro world is due, we believe, to the difficulty of fabricating a truly three dimensional object such as a coil spring (as opposed to a multi-layer structure with vertical connections between layers).

The closest prior art to (though still substantially different from) the present invention that we have found are a pair of patents by Little (US 5,663,596 - structure) and (US 5,665,648 - method). In Little's invention, chip contacting pads are located on the vertical edges of the integrated circuit chip. The latter is attached to a substrate through spring contacts that have a base portion joined to a line on the substrate and a spring portion that presses against the edge of the chip. To fabricate the spring, a release layer is first laid down. Then a metal film is deposited under conditions known to induce high tensile stress so that, when the release layer is etched away, the freed metal curls up to a sufficient degree to allow the chip to be pushed against its underside, thereby forming a spring contact.

While Little's structure is not a coil, coil-like structures have been described in the prior art for use as micro inductors. These structures are, however, not truly three dimensional and comprise multiple layers of open rings that are then connected in series by means of vertical vias. An example of this is described by Lee (US 5,831,331). Such structures, while working as inductors, could not be used to provide stress relief as each ring is separated from its neighbors by a rigid layer. Even if the latter was made to be flexible, absorption of stress would be limited to the vias which have very little flexibility so that any, except very small, stresses would be transmitted between contacting surfaces.

SUMMARY OF THE INVENTION

It has been an object of the present invention to provide a structure capable of absorbing stress between an integrated circuits package and the semiconductor.

Another object of the invention has been to provide a process for manufacturing said structure.

A still further object of the invention has been that said process be fully compatible with existing manufacturing techniques for integrated circuits.

These objects have been achieved by introducing a micro-spring between the solder ball and the contact tab. The process for manufacturing such a structure begins with a fully completed integrated circuit on whose surface freestanding metal posts are formed, each post being in contact with an I/O tab. Using a leveling plate at elevated temperature, the posts are given a permanent tilt relative to the surface and are then encapsulated in an elastomer. This subprocess may then be repeated as many times as desired with the direction in which the posts lean being changed by 90 degrees at each iteration. This results in the formation of an orthogonal spiral which acts as a coil spring that absorbs stress originating at the solder ball.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 shows a silicon wafer with a fully completed integrated circuit which is the starting point for the process of the present invention. It has been coated with a blanket layer of metal followed by layer of photoresist.

FIG. 2 shows how freestanding metal posts are formed after the holes in the photoresist have been filled by electroplating and the photoresist stripped away.

FIG. 3 shows how a leveling plates is used to tilt the metal posts.

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FIG. 4 is FIG. 3 after encapsulation in an elastomer.

FIG. 5 illustrates how multiple iterations of the basic process may be performed.

FIG. 6 shows a structure limited to 2 iterations followed by the formation of solder balls on the top surface.

FIG. 7 is an example of a structure made with four iterations.

FIG. 8 shows an isometric view of an orthogonal spiral that was formed by using the process of the present invention.

FIG. 9 is similar to FIG. 7 except for the addition of joint strengthening pads at the intersections of the posts.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

We will disclose the present invention through a description of the process for its manufacture. This will also serve as a description of the structure of the invention.

Referring now to FIG. 1, the process of the present invention begins with the

provision of silicon wafer 1 which has reached the end of the IC manufacturing cycle so that it contains at least one fully completed integrated circuit, including a final passivation layer (if used) and, particularly, contact pads on the surface that connect to various points inside the integrated circuit. A blanket layer 5 of metal such as gold, copper, or silver is deposited on the surface of wafer 1 to a thickness between about 500 and 7,000 Angstroms. This is then coated with photoresist; layer 2 to a thickness between about 5 and 200 microns. This particular thickness is carefully controlled since it will determine the lengths of the various posts (or rods) that make up the structure. Using standard photolithographic techniques, layer 2 is then processed to form a mask that is present everywhere except for holes, such as 3, over the contact pads. The diameter of these holes, which is typically between about 5 and 100 microns, is also carefully controlled since this will determine the diameters of posts. Electrical contact is made to the blanket layer 5 so that, by means of electroplating, holes 3 can be filled with metal. Our preferred metal for this has been gold but other metals such as copper, solder, silver, or aluminum could also have been used.

Referring now to FIG. 2, the photoresist is now removed, leaving behind free standing metal posts 21. Next, blanket layer 5 is removed by etching in KI/I_2 solution (in the case of gold) just long enough to not affect posts 21 (typically between about 1 and 20 minutes). Note that the posts have a slightly thickened appearance at their base. This is achievable through appropriate use of the negative photoresist. Although not explicitly illustrated, each post 21 is now attached to a single contact pad.

A key feature of the invention is illustrated in FIG. 3. After heating the structure to a temperature between about 100 and 450 °C, leveling plate 35 is lowered onto the structure so that its under-surface is in uniform contact with the top ends of all metal posts 21. A leveling plate needs to have a melting point greater than that of the post metal and must not react with it. Typically a stainless steel plate about 2 cm. thick, having a surface that is optically flat, has been preferred. Through robotic control, a uniform force is applied to the top surface 35. This force (symbolized by arrows 36) is at an angle of approximately 45° to the horizontal, resulting in posts 21 becoming tilted at an angle of between about 15 and 75 degrees relative to the substrate. Since the chosen temperature is above the point at which plastic flow of the metal begins, once tilted and then cooled down again, posts 21 retain their orientation relative to the surface after the leveling plate 35 is removed.

Once the sloping posts have been formed, with the leveling plate still in position they are encapsulated in elastomer layer 42, as shown in FIG. 4, after the leveling plate has cooled down. The structure is placed in a moderate vacuum (around 0.1 torr), elastomer material, such as silicone elastomer, is dispensed along the wafer edge, following which the pressure is returned to atmospheric, allowing the elastomer to be sucked into the gap. Once the elastomer has filled all empty space between the leveling plate and the wafer surface, the former is removed. Thus, while the posts are fully covered, their free ends are left uncovered. Our preferred elastomer material has been silicone elastomer, but other materials such as polyimides or benzocyclobutene (BCB) could also have been used.

Referring now to FIG. 5, the above process is repeated so that posts 22 are formed, each member of 22 being attached to a free (uncovered) end of a post 21. As before, a leveling plate is used on metal posts 22 to tilt them away from the vertical. Although the angle of tilt is the same as before, the direction in which the posts are pushed is such that posts 22 end up pointing in a direction that is orthogonal to that to which posts 21 point.

This is schematically illustrated in FIG. 6 where posts 21 have been drawn with broken lines to signify that they are pointing in a direction that is normal to the plane of the figure while posts 22 are drawn with solid lines to signify that they lie in the plane of the figure. If desired, the process of the present invention could be terminated at this point. If this option is elected, then, following the introduction of second elastomer layer 43, contacts to the outer world are formed at the top surface of 43. First, pads 61 of an underlayer barrier metal such as chromium/chrome-copper/copper or similar copper metal systems are formed over all uncovered ends of posts 22 following which solder balls 62 are grown over and around pads 61 by means of electroplating or screen printing. Our preferred material for the solder balls has been eutectic solder but other compositions including high temperature solders, $\text{Sn}_5\text{Pb}_{95}$, or other no lead solder, such as indium-tin could also have been used. The solder balls had diameters between about 10 and 500 microns and are spaced, on average, a distance of between about 10 and 1,000 microns apart.

In the likely event that it was elected to not terminate the process after only two iterations, the above described steps may be repeated as many times as desired (in most

cases this would be up to about 4 additional times). In FIG. 7 we illustrate a schematic cross-section of a structure in which 4 iterations were performed. As in FIG. 6, broken lines signify that posts are pointing into (or out of) the plane of the figure. Note that although posts 24 have been drawn with solid lines, they are not actually in the plane of the figure but in a plane parallel to it.

FIG. 8 is an isometric view of the structure of the present invention. Five iterations of post formation are shown in this case. Top surface barrier pads and solder balls are not shown in the figure and the successively formed layers of elastomer are shown with broken lines although, in practice, they would form a single uniform layer in the final structure. The illustration shows how, through application of the present invention, a structure, functionally equivalent to a coil spring has been formed. We refer to this as an 'orthogonal spiral' since it is made up of straight line segments that are orthogonally disposed relative to one another.

In FIG. 9 we show an alternative optional embodiment of the invention. As in the previously described structures, that of FIG. 9 shows a series of posts (rods) 21 through 24 joined to form an orthogonal spiral. They have been encapsulated in elastomer through successive application of layers 42-45. The key feature of this embodiment is the presence of joint strengthening discs 91 between the rods. These discs were formed, in each iteration, directly after the introduction of the elastomer. At that point in the process a layer of joint strengthening metal such as gold, copper, or nickel was deposited onto the

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elastomer. It was then patterned and etched to form joint strengthening discs 91 which were symmetrically disposed on and around the uncovered ends of the rods.

While the invention has been particularly shown and described with reference to the preferred embodiments thereof, it will be understood by those skilled in the art that various changes in form and details may be made without departing from the spirit and scope of the invention.

What is claimed is:

1. A process for forming a stress release contacting system in an integrated circuit, comprising the sequential steps of:

(a) providing a silicon wafer containing a completed integrated circuit and having an upper surface on which are contact pads connected to said integrated circuit;

5 (b) forming first metal posts, attached one-on-one to the contact pads and extending vertically upwards from the contact pads;

(c) placing a leveling plate on said metal posts;

(d) through application of force to said leveling plate at an elevated temperature, causing the posts to tilt at an angle relative to said wafer upper surface and to point in a direction;

10 (e) filling all empty space between the leveling plate and the wafer surface with an elastomer while leaving all ends of the posts uncovered;

(f) removing the leveling plate;

(g) forming second metal posts that attach one-on-one to said uncovered ends;

15 (h) placing a leveling plate on said metal posts;

(i) through application of force to said leveling plate at an elevated temperature, causing the posts to tilt at said angle relative to said wafer upper surface and to point in a direction that is orthogonal to the direction of the most recently formed posts;

(j) repeating steps (e) through (i) a number of times;

20 (k) filling all empty space between the leveling plate and the wafer surface with an elastomer while leaving all ends of the posts uncovered;

(l) removing the leveling plate;

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(m) forming pads of underlayer barrier metal over all uncovered ends of said posts;

and

(n) forming solder balls that extend upwards and are attached to the underlayer barrier metal pads.

5 2. The process of claim 1 wherein the step of forming the metal posts further comprises:

depositing a blanket layer of metal;

coating said metal layer with photoresist;

processing the photoresist to form a mask that is present everywhere except for

10 holes over the contact pads;

by means of electroplating, filling said holes with the metal;

removing the photoresist, thereby leaving metal posts; and

removing the blanket layer.

15 3. The process of claim 1 wherein said elevated temperature is between about 100 and 400 °C .

4. The process of claim 1 wherein said tilt angle is between about 15 and 75 degrees.

5. The process of claim 1 wherein the step of filling with elastomer further comprises:
after the leveling plate has cooled down, placing the wafer in a vacuum having a

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pressure around 0.1 torr;

dispensing elastomer along all edges of the wafer; and

returning the pressure to atmospheric, thereby causing the elastomer to be sucked into said empty space.

- 5 6. The process of claim 1 wherein the posts have a diameter between about 5 and 100 microns.
7. The process of claim 1 wherein the posts have a length between about 5 and 200 microns.
- 10 8. The process of claim 1 wherein the number of times that steps (e) through (i) are repeated is between 0 and about 10 times .
9. The process of claim 1 wherein the elastomer is selected from the group consisting of silicone elastomer, polyimides and benzocyclobutene.
10. The process of claim 1 wherein the metal posts are selected from the group consisting of gold, silver, copper, solder, and aluminum.
- 15 11. The process of claim 1 further comprising, between steps (f) and (g):
depositing a layer of joint strengthening metal on the elastomer; and

patterning and etching said layer to form joint strengthening discs symmetrically disposed on and around said uncovered ends.

12. A low stress contacting system for an integrated circuit, comprising:

a silicon wafer containing a completed integrated circuit and having an upper surface

5 on which there is contact pad connected to said integrated circuit;

a number of metal rods, including a first rod and a last rod, attached end-to-end to each other in the form of an orthogonal spiral;

all rods being tilted at an angle relative to said upper surface;

the first rod being attached to a contact pad;

10 the last rod being attached to a solder ball;

elastomer, covering said upper surface and extending upwards therefrom to fully encapsulate all the rods while leaving said solder ball uncovered; and

an underlayer barrier metal pad between the solder ball and the last rod.

13. The contacting system of claim 12 wherein the elastomer is selected from the group
15 consisting of silicone elastomer, polyimides and benzocyclobutene.

14. The contacting system of claim 12 further comprising joint strengthening discs between the rods.

15. The contacting system of claim 12 wherein said number of rods is between 2 and

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about 10.

16. The contacting system of claim 12 wherein the angle at which the rods are tilted is between about 15 and 75 degrees.

5 17. The contacting system of claim 12 wherein the rod has a diameter between about 5 and 100 microns and a length between about 5 and 200 microns.

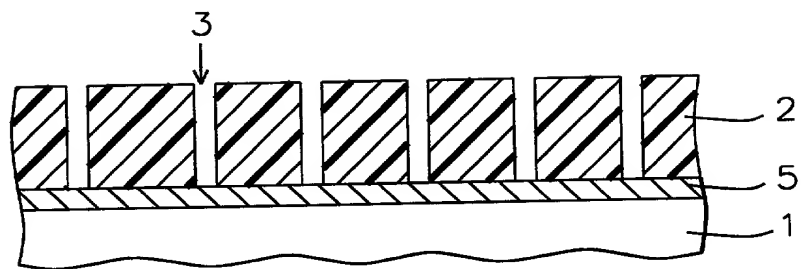
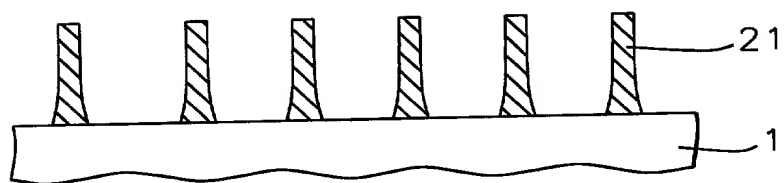
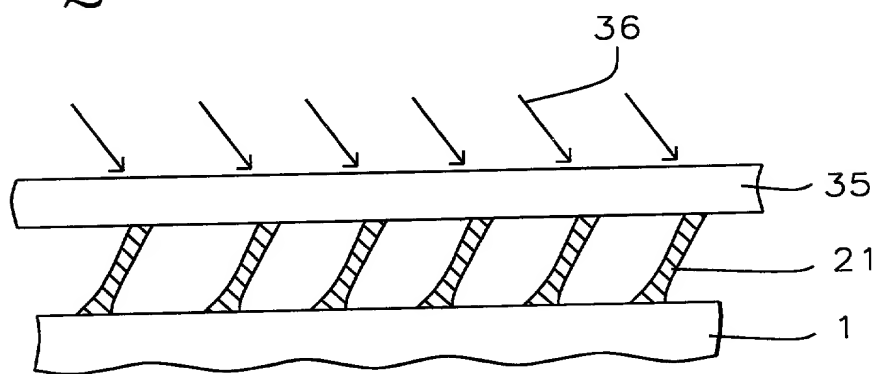
18. The contacting system of claim 12 wherein the solder balls have a diameter between about 10 and 500 microns.

19. The contacting system of claim 12 wherein the solder balls are selected from the group consisting of eutectic solder, high temperature solders, and no lead solders.

10 20. The contacting system of claim 12 wherein the solder balls are spaced, on average, a distance of between about 10 and 1,000 microns apart.

ABSTRACT

The problem of stress transmission from the outside of an integrated circuit package into the interior of the semiconductor has been significantly reduced by placing a micro-spring between the external solder ball and the interior tab. The process for manufacturing such a structure begins with a fully completed integrated circuit on whose surface freestanding metal posts are formed, each post being in contact with an I/O pad. Using a leveling plate at elevated temperature, the posts are given a permanent tilt relative to the surface and are then encapsulated in an elastomer. This subprocess may then be repeated as many times as desired with the direction in which the posts lean being changed by 90 degrees at each iteration. This results in the formation of an orthogonal spiral which acts as a coil spring to absorb stress originating at the solder ball.

*FIG. 1**FIG. 2**FIG. 3*

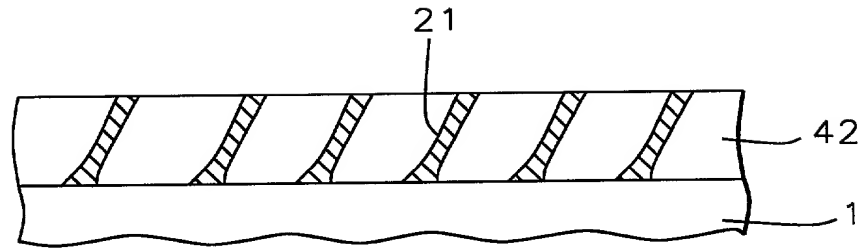


FIG. 4

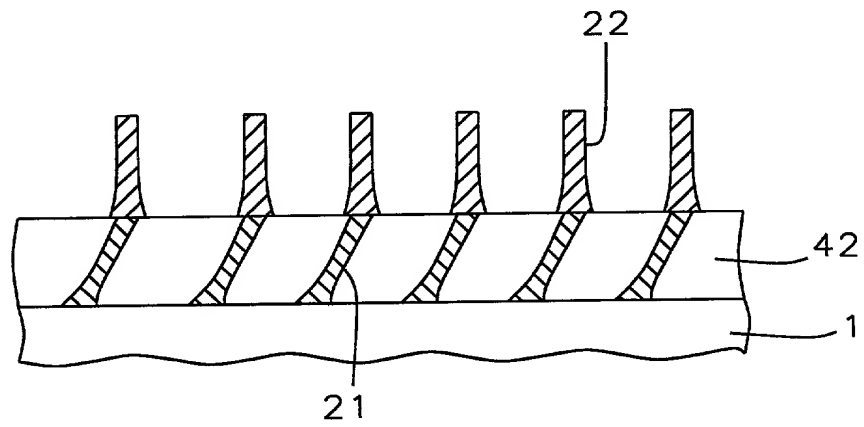


FIG. 5

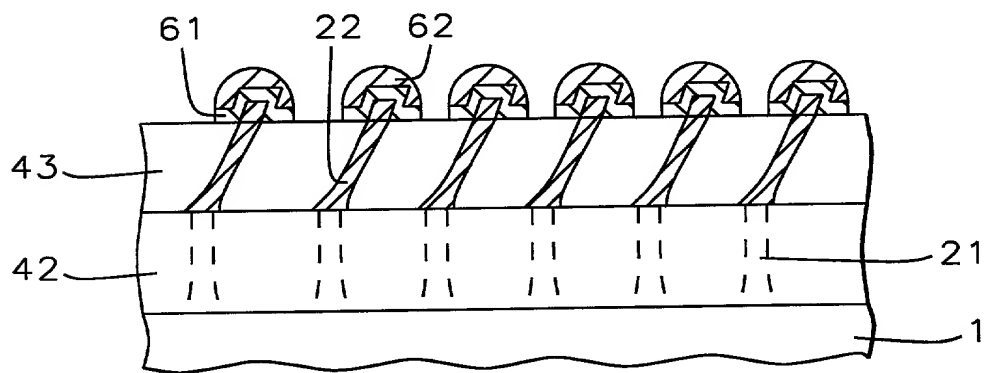


FIG. 6

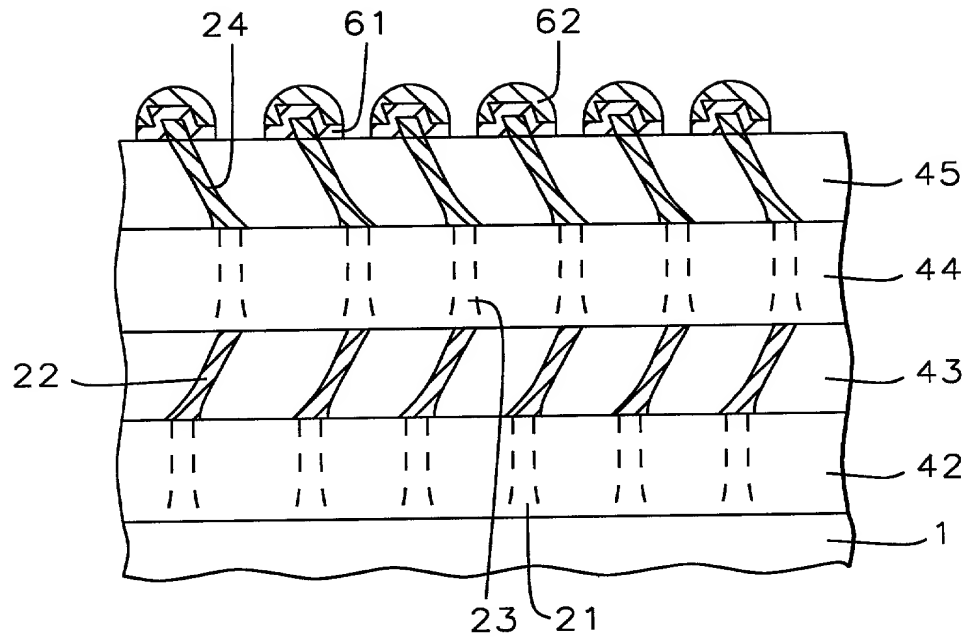


FIG. 7

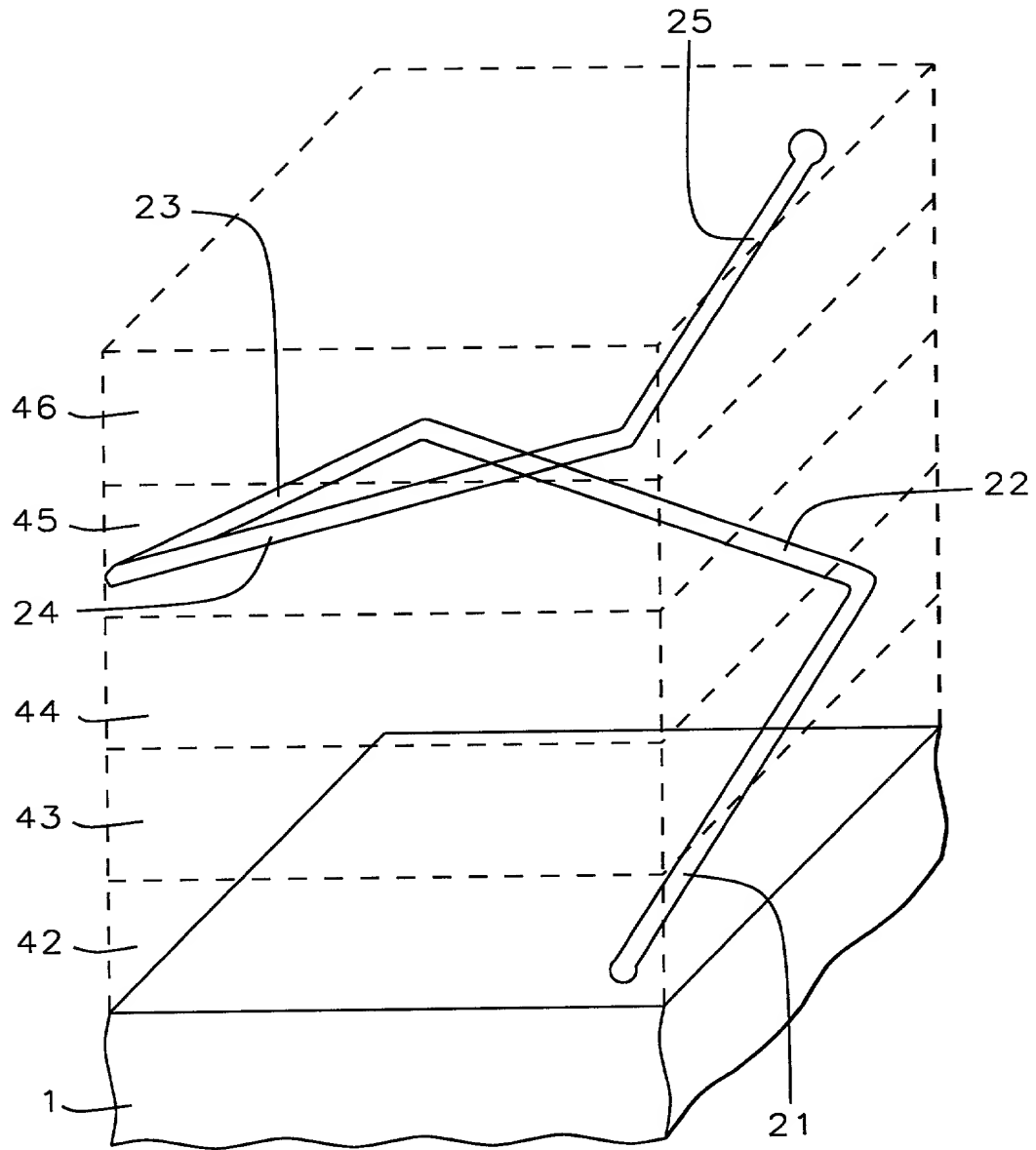


FIG. 8

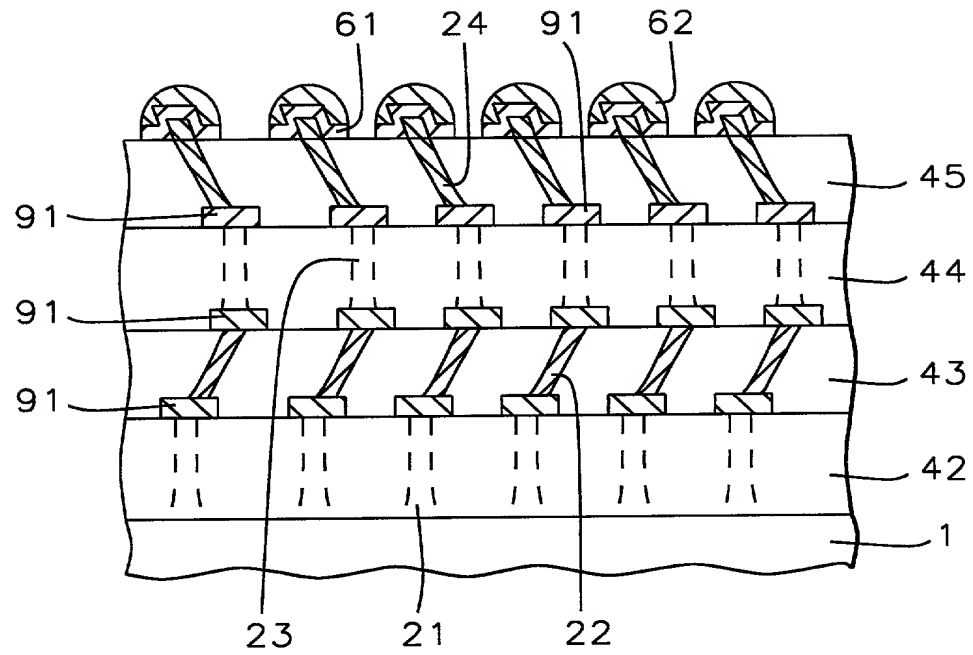


FIG. 9

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

DOCKET NO. MSLIN98-003

As a below named Inventor, I hereby declare that

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled Strain Release Contact System For Integrated Circuits

the specification of which (check one)

☒ X is attached hereto.

was filed on _____

Application Serial No. _____

and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above Identified specification including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed:

(Number)	(Country)	(Day/Month/Year Filed)
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(Number)	(Country)	(Day/Month/Year Filed)
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I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status) (patented, pending, abandoned)
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name & registration no.)

GEORGE O. SAILE. (Reg. No. 19,572), STEPHEN B. ACKERMAN (Reg. No. 37,761)

Send Correspondence to: 20 MCINTOSH DRIVE, POUGHKEEPSIE, NEW YORK 12603

Direct telephone Calls to: (name & telephone number) GEORGE O. SAILE NEW YORK 914 452 5863

Mou-Shiung Lin January 18, 1999
Full name of sole or first inventor Date

Mou-Shiung Lin 1/18/1999
Inventor's signature

24 the 3rd Bamboo Rd, 6F, Hsinchu, Taiwan
Residence

USA
Citizenship

24 the 3rd Bamboo Rd, 6F, Hsinchu, Taiwan
Post Office Address